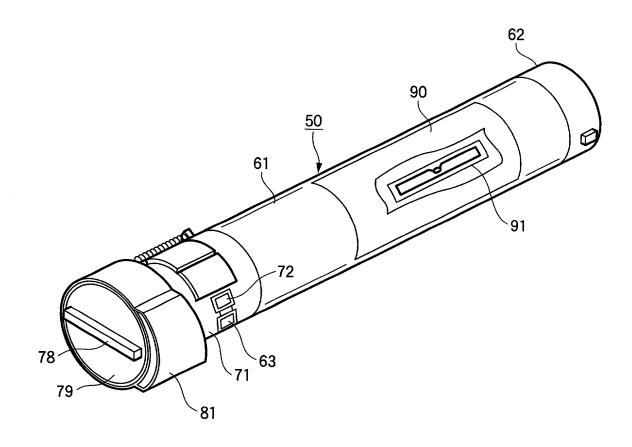
FIG.1



****20 ~26 **EXPOSURE DEVICE** 2 <u>1</u>0 $\frac{1}{2}$ 5%93 92 5K 52

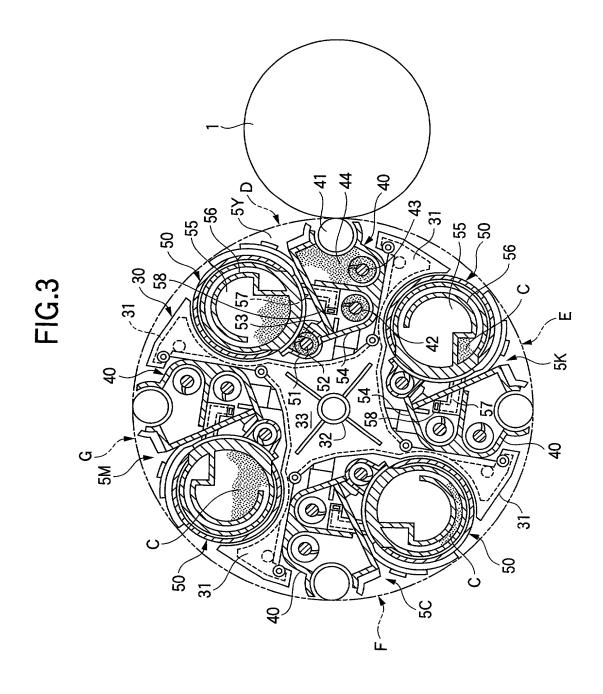


FIG.4

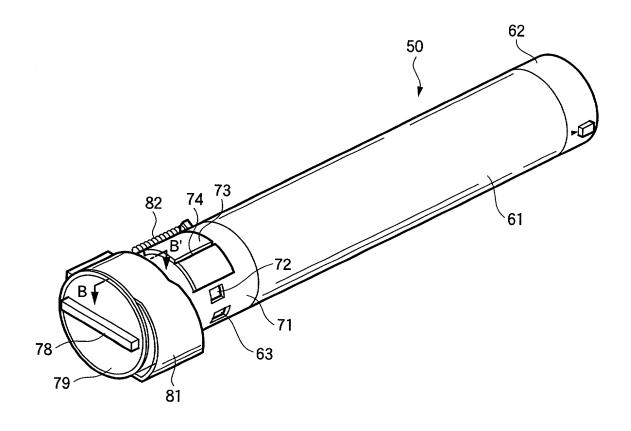


FIG.5

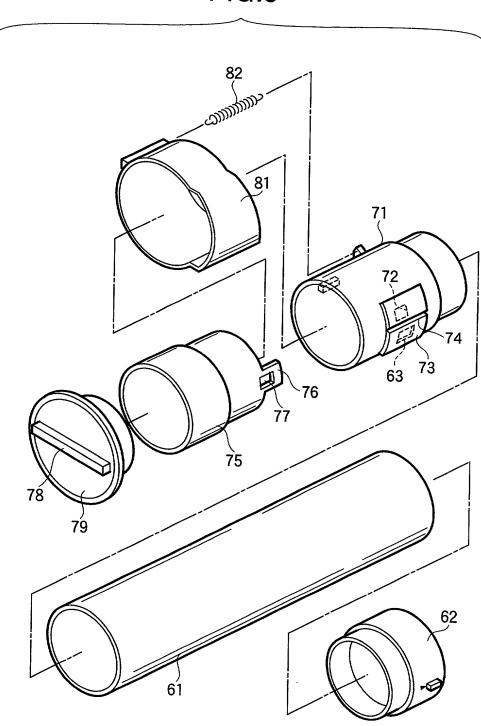


FIG.6

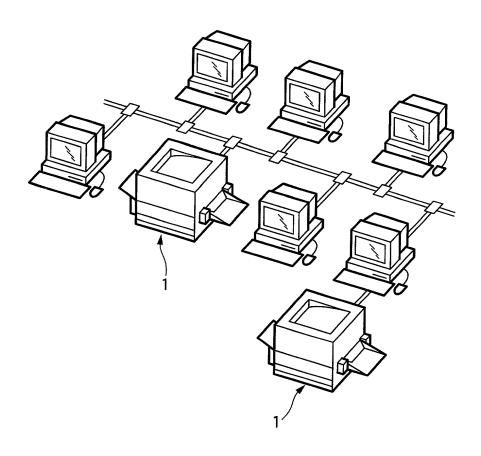


FIG.7

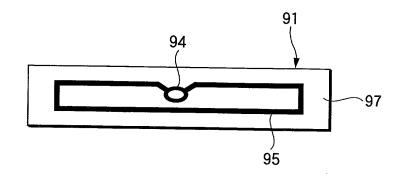


FIG.8

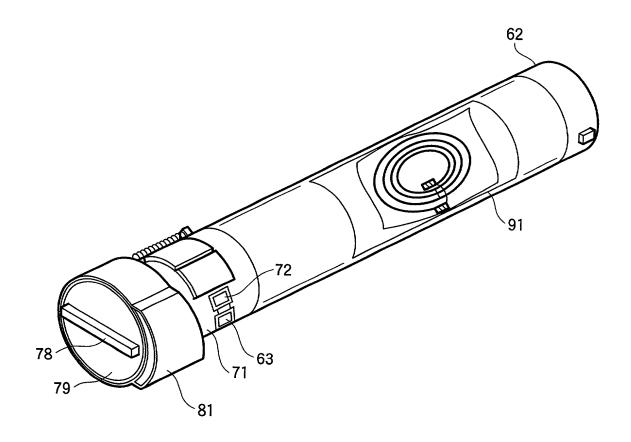


FIG.9

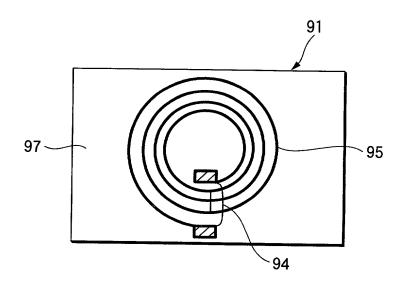


FIG.10

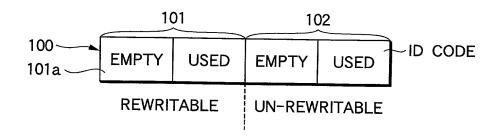


FIG.11

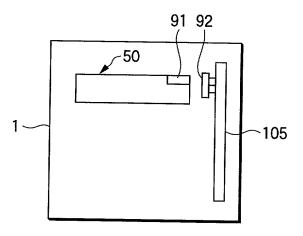
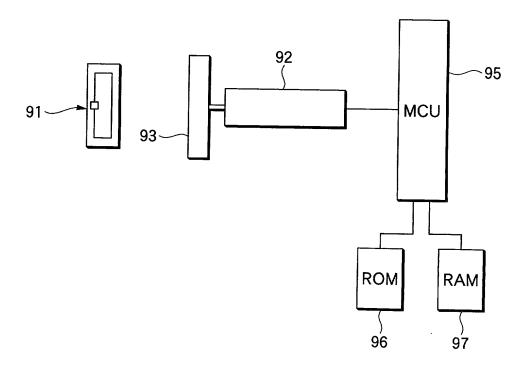


FIG.12



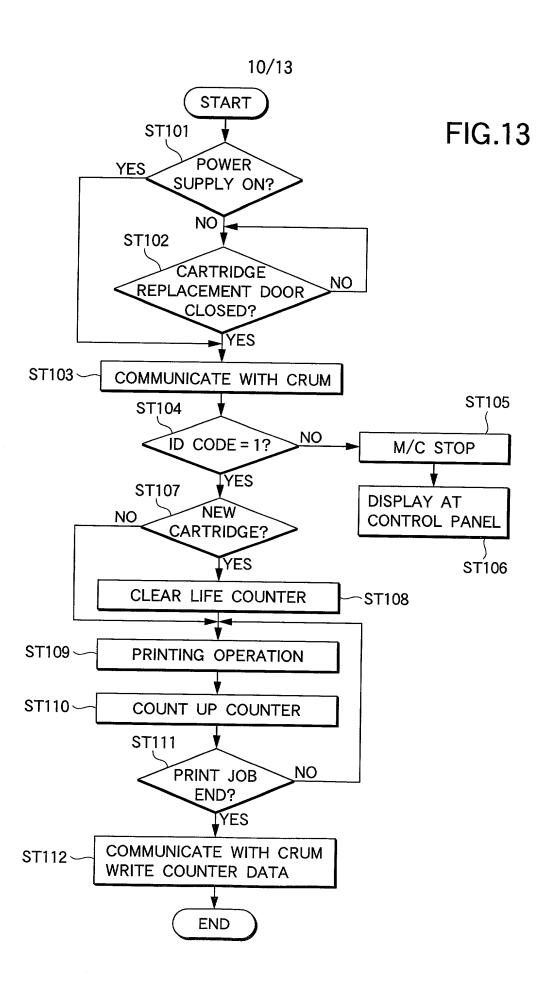


FIG.14

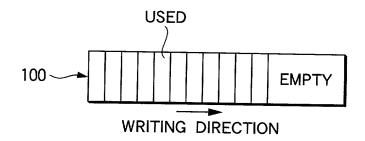


FIG.15

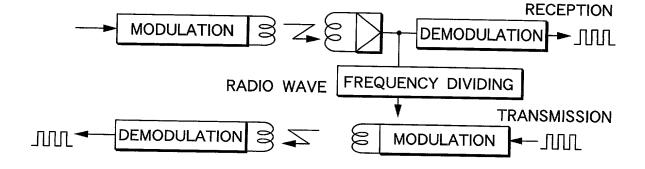


FIG.16 111 95 110 POWER SUPPLY CIRCUIT 113(94) MODULATION **DEMODULATION** STORAGE **CIRCUIT CIRCUIT ELEMENT** 114 H112 CPU 115

FIG.17

